

numerous scientific, business, and consumer applications. The Internet of Things (IoT) paradigm promises to make any electronic devices part of the Internet environment. This new paradigm opens the doors to new innovations and interactions between people and things that will enhance the quality of life and utilization of scarce resources. To help realize the full potential of IoT, the book addresses its numerous challenges and develops the conceptual and technological solutions for tackling them. These challenges include the development of scalable architecture, moving from closed systems to open systems, designing interaction protocols, autonomic management, and the privacy and ethical issues around data sensing, storage, and processing. Addresses the main concepts and features of the IoT paradigm Describes different architectures for managing IoT platforms Provides insight on trust, security, and privacy in IoT environments Describes data management techniques applied to the IoT environment Examines the key enablers and solutions to enable practical IoT systems Looks at the key developments that support next generation IoT platforms Includes input from expert contributors from both academia and industry on building and deploying IoT platforms and applications

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Papers from the September 1995 symposium cover aspects of computer-aided synthesis of complete electronic systems and design automation of complex integrated systems, highlighting areas such as hardware/software codesign; retargetable code; software synthesis for embedded processors; behavioral synthesis; estimation techniques; and design methods for mixed hardware/software systems. Includes material on multimedia system design. No index. Annotation copyright by Book News, Inc., Portland, OR.

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"Digital Design provides a modern approach to learning the increasingly important topic of digital systems design. The text's focus on register-transfer-level design and present-day applications not only leads to a better appreciation of computers and of today's ubiquitous digital devices, but also provides for a better understanding of careers involving digital design and embedded system design. The book's key features include: An emphasis on register-transfer-level (RTL) design, the level at which most digital design is practiced today, giving readers a modern perspective of the field's applicability. Yet, coverage stays bottom-up and concrete, starting from basic transistors and gates, and moving step-by-step up to more complex components. Extensive use of basic examples to teach and illustrate new concepts, and of application examples, such as pacemakers, ultrasound machines, automobiles, and cell phones, to demonstrate the immediate relevance of the concepts. Separation of basic design from optimization, allowing development of a solid understanding of basic design, before considering the more advanced topic of optimization. Flexible organization, enabling early or late coverage of optimization methods or of HDLs, and enabling choice of VHDL, Verilog, or SystemC HDLs. Career insights and advice from designers with varying levels of experience. A clear bottom-up description of field-programmable gate arrays (FPGAs). About the Author: Frank Vahid is a Professor of Computer Science & Engineering at the University of California, Riverside. He holds Electrical Engineering and Computer Science degrees; has worked/consulted for Hewlett Packard, AMCC, NEC, Motorola, and medical equipment makers; holds 3 U.S. patents; has received several teaching awards; helped setup UCR's Computer Engineering program; has authored two previous textbooks; and has published over 120 papers on digital design topics (automation, architecture, and low-power).

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